



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Tatsuhiro Mizumasa**

Serial Number: 10/098,535

Group Art Unit: 2812

Filed: March 18, 2002

Examiner: Stacy Whitmore

**For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING A PLURALITY
OF CIRCUIT REGIONS WHERE DIFFERENT POWER SUPPLY VOLTAGES
ARE USED AND METHOD OF MANUFACTURING THE SAME**

DECLARATION UNDER 37 CFR 1.132

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: _____

Sir:

The Undersigned hereby declare that:

U.S. Patent No. 6,584,608 with Touru Kumada, Takao Yamaguchi, Junichi Kigoshi, and
Kazunori Kumagai as inventors; and

U.S. Patent Application No. 10/098,535 with Tatsuhiro Mizumasa as inventor,

were, at the time the invention was made, owned by, or subject to an obligation of assignment
to, Fujitsu Limited, Kawasaki, Japan.

Under MPEP 706.02(I)(2), the evidence required to establish common ownership of U.S.
Patent Application No. 10/098,535 and U.S. Patent No. 6,584,608 is deemed to be sufficient and
satisfied.

The undersigned declares that all statements made herein of his/her own knowledge are true
and that all statements made on information and belief are believed to be true; and further that these
statements were made with the knowledge that willful false statements and the like so made are
punishable by fine or imprisonment, or both, under § 1001 of Title 18 of the United States Code and
that willful false statements may jeopardize the validity of the application or any patent issued thereon.

Tatsuhiro Mizumasa
Tatsuhiro Mizumasa

Signed this 2nd day of February, 2004.